

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

Takeshi SHIOGA, et al.

Serial No. Group Art Unit:

Confirmation No.

Filed: July 30, 2003 Examiner:

For: CAPACITOR AND SEMICONDUCTOR DEVICE AND METHOD FOR FABRICATING  
THE SEMICONDUCTOR DEVICE

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

Before examination of the above-identified application, please amend the application as follows:

**IN THE CLAIMS:**

The text of all pending claims are set forth below. Cancelled and withdrawn claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with strikethrough. The status of each claim is indicated with one of (original), (currently amended), (previously amended), (cancelled), (withdrawn), (new), (previously added), (reinstated - formerly claim #), (previously reinstated), (re-presented - formerly dependent claim #) or, (previously re-presented). Please CANCEL claims 1-12, 14 and 15 and AMEND claim 13 in accordance with the following:

1-12 (cancelled)

13. (currently amended) A capacitor according to claim 1 comprising a first conducting film formed on a substrate, a first dielectric film formed on the first conducting film, a second conducting film formed on the first dielectric film, a second dielectric film formed above the second conducting film, covering an edge of the second conducting film, and a third conducting